

**WEST**

## Freeform Search

**Database:**

US Patents Full-Text Database  
US Pre-Grant Publication Full-Text Database  
JPO Abstracts Database  
EPO Abstracts Database  
Derwent World Patents Index  
IBM Technical Disclosure Bulletins

**Term:**

L14 and (((writ\$3 or stor\$3) adj2 register) with  
(ID or identifier or identification))

**Display:**  Documents in **Display Format:**  Starting with Number **Generate:** ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search

Clear

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Interrupt

Main Menu

Show S Numbers

Edit S Numbers

Preferences

Cases

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### Search History

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**DATE:** Tuesday, August 05, 2003   [Printable Copy](#)   [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

*DB=USPT; PLUR=YES; OP=OR*

<u>L19</u>	L14 and (((writ\$3 or stor\$3) adj2 register) with (ID or identifier or identification))	45	<u>L19</u>
<u>L18</u>	L14 and ((writ\$3 or stor\$3) with (register) with (ID or identifier or identification))	170	<u>L18</u>
<u>L17</u>	L14 and ((writ\$3 or stor\$3) with (standby or hibernat\$3 or sleep\$3) with (ID or identifier or identification))	10	<u>L17</u>
<u>L16</u>	L13 and ((writ\$3 or stor\$3) with (standby or hibernat\$3 or sleep\$3) with (ID or identifier or identification))	15	<u>L16</u>
<u>L15</u>	L13 and ((writ\$3 or stor\$3) with (standby or hibernat\$3 or sleep\$3))	233	<u>L15</u>
<u>L14</u>	((365/\$)!.CCLS.)	33117	<u>L14</u>
<u>L13</u>	((711/\$)!.CCLS.)	14779	<u>L13</u>
<u>L12</u>	L11 and ((writ\$3 or stor\$3) with (standby or hibernat\$3 or sleep\$3))	349	<u>L12</u>
<u>L11</u>	((713/\$)!.CCLS.)	10759	<u>L11</u>
<u>L10</u>	((713/ )!.CCLS. )	0	<u>L10</u>
<u>L9</u>	15 and ((writ\$3 or stor\$3) with (standby or hibernat\$3))	7	<u>L9</u>
<u>L8</u>	15 and ((writ\$3 or stor\$3) with sleep\$3)	3	<u>L8</u>
<u>L7</u>	16 and sleep\$3	1	<u>L7</u>
<u>L6</u>	L5 and ((semiconductor or (integrated adj2 circuit\$2)) with memory with (cpu or processor))	46	<u>L6</u>
<u>L5</u>	13 or L4	608	<u>L5</u>
<u>L4</u>	((365/52 )!.CCLS. )	326	<u>L4</u>
<u>L3</u>	((711/104 )!.CCLS. )	285	<u>L3</u>
<u>L2</u>	L1 and (cpu or processor)	0	<u>L2</u>
<u>L1</u>	3934233.pn.	1	<u>L1</u>

END OF SEARCH HISTORY